

ANANTHA LAKSHMI
INSTITUTE OF TECHNOLOGY AND SCIENCES



NEWS LETTER

VOLUME:02

ISSUE:01



ELECTRONICS AND COMMUNICATION ENGINEERING

ELECTRO-NEWS

2021-22 Even Semester News

ELECTONICS AND COMMUNICATION ENGINEERING

Vision of the Department

To produce professional engineers in Electronics and Communication with Ethical consciousness to meet local and global demands.

Mission of the Department

M1: Providing an ambience of teaching-learning process through the state-of-art infrastructure and modern tools for imparting latest technical skills among the students.

M2: Providing student-centric facilities and training for the students to acquire a strong collaboration with Electronics Industry.

M3: Providing value-based education to find solutions for societal needs.

Program Specific Outcomes (PSO'S)

Students must be able to:

PSO1: Design and develop customized electronic circuits for domestic and industrial applications.

PSO2: Analyze and design the Digital Systems in Electronics using hardware and modern tools and full fill the gap between academics and industry.

PSO3: Apply professional ethics and ethical responsibility to a successful career in open-ended problems of Electronics and Communication Engineering by adhering to technological changes.

About the Department

Electronics & Communication Engineering can be treated as universal branch among all circuit branches. ECE branch is the most sophisticated, ever demanding and fast growing branch in the engineering curriculum. It covers a wide range of applications which makes our life easier, secure and enjoyable such as Mobile, Television, Radio, Computer, Radar, Satellite etc. The course is useful to design and manufacturing of electronic systems and appliances. Electronic engineers are eligible for opportunities in Hardware, Software and Public Sector Units. The engineers are also employable in IC fabrication and Embedded system development industries.

1: Graduates will be able to acquire the competent knowledge in Electronics and Communication Engineering.

2: Graduates will be able to have successful entrepreneurial and professional engineers in the core and allied areas of electronics and communication engineering.

3: Graduates will possess best communicative skills and work efficiently on team-based projects in electronics, communication, with a sense of social responsibility.

PROGRAM EDUCATIONAL OUTCOME'S (PEO'S)

- Graduates can acquire competent knowledge in Electronics and Communication Engineering.
- Graduates can have successful entrepreneurial and professional careers in the core and allied areas of electronics and communication engineering.
- Graduates can possess communication skills and work efficiently on team-based projects in electronics, and communication with a sense of social responsibility.

ABOUT DEPARTMENT

Electronics and communication Engineering is at the core of the information age. To prepare our students for the tremendous opportunities in the field, the ECE Department is strongly committed to excellence in both education and research. Our majors are designed to provide a strong foundation in the core areas of Electronics and communication Engineering.

Our vibrant graduate programs prepare students for positions in industry and academia. Since its inception, the department has always been recognized for excellence in teaching. The Department provides an outstanding teaching environment complemented by superior teaching for its students to flourish in. Graduates from the department are recruited by both academia and industry.

The Department of Electronics and communication Engineering with its cohesive team of faculty members offers a sound program at the UG as well as the PG levels. The curriculum is a blend of the conventional and the radical. It is updated regularly to keep up with the growing demands and the changing trends of the software industry and research laboratories.

MESSAGES

CHAIRMAN'S MESSAGE



Sri. M AnanthaRamudu
C hairman ALTS, ATP

ALTS have made tremendous progress in all areas crossing several milestones within a very short span of time. And now I feel very happy to know that the students and faculty of the ECE department of ALTS are bringing out the first volume of the Technical magazine E-Magazine. As I understand, this magazine is intended to bring out the inherent literary talents in the students and the teachers and also to inculcate leadership skills among them. I am confident that this issue will send a positive signal to the staff, students and the persons who are interested in the educational and literary activities.

VICE CHAIRMAN'S MESSAGE

It gives me immense pleasure to pen a few words as prologue to the first volume of the technical magazine **E-Magazine** exclusively meant for churning out the latent writing talent which bears immense potentiality of sharpening the student's skills as part of their overall personality development. I congratulate all the contributors for bringing out such a beautiful magazine.



Sri. M Ramesh Naidu
Vice Chairman ALTS,
ATP

PRINCIPAL'S MESSAGE



Dr. Moorthy Rao Khokhalay
Principal, ALTS, ATP.

I congratulate the department of ECE, ALTS for bringing out the first issue of the prestigious department technical Magazine, **E-Magazine**. I am sure that the magazine will provide a platform to the students and faculty members to expand their technical knowledge and sharpen their hidden literary talent and will also strengthen the all-round development of the students. I am hopeful that this small piece of literary work shall not only develop the taste for reading among students but also develop a sense of belonging to the institution as well. My congratulations to the editorial board who took the responsibility for the arduous task most effectively. I extend best wishes for the success of this endeavour.

DEPARTMENT ACTIVITIES

1. TECHNICAL SYMPOSIUM (e-Udbhav-2K22)

Department of ECE has organized a National Level Technical Symposium “e-Udbhav-2k22” on 08-06-2022- to 09-06-2022. The chief guest for the program was Sri Dr. S Chandra Mohan Reddy garu, Associate Professor of ECE & Controller of Examination (PG), JNTU Ananthapuramu, Ananthapuramu. A total of 10 events were organized as listed below:

Anantha Lakshmi
Institute of Technology and Sciences
Accredited by NAAC, Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
Presents
E-UDBHAV 2K22
A BEGINNING THAT NEVER ENDS

Follow us on
@E_udbhav
E Udbhav
E_udbhav
e_udbhav

Poster Presentation
Paper Presentation
Project Expo
Circuit Hunt

On June 8th & 9th

Chief Patrons
Sri. M. Anantha Ramudu, B.A.,
Chairman
Sri. M. Ramesh Naidu, M.S., (USA)
Vice Chairman

Patron
Dr. V. Murthy Rao Khokalay
M.Tech., Ph.D., M.Sc. (Env.scl),
M.I.A.H., M.I.W.R.S, M.I.M.S.,
Principal

Convener
Dr. M.V. Sreeraj
M.Tech., Ph.D.
HOD of ECE



Reg. fee for any event Rs. 200/-
email id : eudbhav2k22@gmail.com

Student Organizers
K. Uday Sai Jaswanth
Cell : 8500008388
Y. Diwakar
Cell : 7659872496



2. FDP/WORKSHOP/STTP

Department of ECE has conducted an offline two-day Workshop to students on “Emerging Trends in Embedded System” by Ananthalakshmi institute of Technology and Science, Ananthapuramu from 07th March to 8th March 2022.

<p>About the College</p> <p>Anantha Lakshmi Institute of technology and sciences (ALITS) is promoted by the Anantha Lakshmi educational Society, which was founded to educate and empower the youth who play a decisive role in shaping the destiny of the nation. The College was established in the year 2008 with an objective to impart quality education to students in the field of Technology and Management studies. The college has all modern facilities and hostels for boys and girls within the campus. The college is affiliated to J.N.T.U.A. Anantapuramu and AICTE New Delhi. ALITS has emerged as one of the premier Engineering College in the state with an overall intake of 1500 students in various UG and PG including B.Tech, M.Tech, and MBA.</p> <p>Vision</p> <p>To produce professional engineers in Electronics and Communication with Ethical consciousness to meet local and global demands</p> <p>Mission</p> <p>M1: To Provide an ambience of teaching-learning process through the state-of-art infrastructure and modern tools for imparting latest technical skills among the students.</p> <p>M2: To Provide Students centric facilities and training for the students to have a strong collaboration with Electronics Industry.</p> <p>M3: To Provide and internalize the value-based education by encouraging the solutions for societal needs.</p>	<p>About ECE Department</p> <p>The ECE program at ALTS started in 2008. Recognized by AICTE and affiliated to JNTUA, Ananthapuramu, the current intake is 180 students. The Department of Electronics and Communication Engineering (ECE) at ALTS symbolizes the ALTS tradition of excellence as a leader in electronics and communication engineering education and research. The Department is dedicated to education, research and overall excellence.</p> <p>A ALTS – ECE graduate is suitably equipped to pursue career in electronics, communication and IT industry, research, or post-graduate studies in the country or abroad. Further through a unique program we promote entrepreneurship in budding engineers.</p> <p>Registration</p> <p>The interested participants have to send their registration form in the prescribed format duly attested by the head of the institution along with the registration fee in the form of a Demand Draft, drawn in favour of Principal, Anantha Lakshmi Institute of Technology and Sciences, and Payable at any Bank in A.P.</p> <p>Registration Fee</p> <table border="1"> <thead> <tr> <th>Category</th> <th>Registration Fee (Per person/paper)</th> </tr> </thead> <tbody> <tr> <td>Participants from</td> <td>Indian [INR]</td> </tr> <tr> <td>R & D Organizations & Industry</td> <td>500</td> </tr> <tr> <td>Faculty from Academic Institutions</td> <td>400</td> </tr> <tr> <td>Research Scholars</td> <td>300</td> </tr> </tbody> </table>	Category	Registration Fee (Per person/paper)	Participants from	Indian [INR]	R & D Organizations & Industry	500	Faculty from Academic Institutions	400	Research Scholars	300	<p><i>In Association with</i></p> <p>THE INSTITUTION OF ELECTRONICS AND TELECOMMUNICATION ENGINEERS, INDIA</p>  <p>A Two Days Workshop On Emerging Trends in embedded System 07th March -08th March, 2022</p> <p>Organized by Department of Electronics and Communication Engineering</p>  <p>Anantha Lakshmi Institute of Technology and Sciences Anantapur-515721 Andhra Pradesh www.alits.ac.in ☎: +91 9000092589</p>
Category	Registration Fee (Per person/paper)											
Participants from	Indian [INR]											
R & D Organizations & Industry	500											
Faculty from Academic Institutions	400											
Research Scholars	300											

3. ADD ON COURSE

Department of ECE has conducted ADD-ON course on “Evaluation in IC Fabrication” for III B.Tech ECE students with 3hrs/week/section.

INDIVIDUAL FACULTY & STUDENT ACTIVITIES

1. PUBLICATIONS

SL NO	Name of Person	Title	Volume	Journal	Year
1.	Dr. Y L Ajay kumar	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Jul-2022
2.	N.Naveen kumar	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Jul-2022
3.	Dr.M.Sreeraj	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Jul-2022
4.	N.Naveen kumar	Design of novel adder for efficient image and video	Volume 13, Issue No:12, :ISSN NO:	Journal of Engineering Sciences	Dec-2022

		processing accelerators	0377-9254		
5.	Dr. Y L Ajay kumar	Design of novel adder for efficient image and video processing accelerators	Volume 13, Issue No:12, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
6.	N.Naveen kumar	Design of innovative 2-4 and 4-16 mixed-logic line decoders using cmos logic gates	Volume XIV, Issue XII, ISSN NO:0022-1945	Journal of inter disciplinary cycle research	Dec-2022
7.	Dr. Y L Ajay kumar	Design of innovative 2-4 and 4-16 mixed-logic line decoders using cmos logic gates	Volume XIV, Issue XII, ISSN NO:0022-1945	Journal of inter disciplinary cycle research	Dec-2022
8.	P.Chandana	Design of high performance VLSI multiplier with redundant binary compression	Volume 13, Issue No:12 :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
9.	Dr. Y L Ajay kumar	Design of high performance VLSI multiplier with redundant binary compression	Volume 13, Issue No:12, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
10.	Y.Nirmala	Design of approximate	Volume XIV, Issue No:XII,	The international	Dec-2022

		multipliers by using hybrid technique	ISSN NO: 0866-936	journal of analytical and experimental modal analysis	
11.	Dr. Y L Ajay kumar	Design of approximate multipliers by using hybrid technique	Volume XIV, Issue No:XII, ISSN NO: 0866-936	The international journal of analytical and experimental modal analysis	Dec-2022
12	B.V.Aravind	Design of high performance prediction logic with dynamic timing error avoidance	Volume IX, Issue No:XII, ISSN NO: 1076-5131	Journal of applied science and computations	Dec-2022
13.	Dr. Y L Ajay kumar	Design of high performance prediction logic with dynamic timing error avoidance	Volume IX, Issue No:XII, ISSN NO: 1076-5131	Journal of applied science and computations	Dec-2022
14	N.Naveen kumar	Design of area-efficient and highly reliable RHBD10T memory cell for aero space applications	Volume IX, Issue No:XII, ISSN NO: 2278-4632	Juni khyat Journal	Dec-2022

15	B.V Aravind	Design of area-efficient and highly reliable RHBD10T memory cell for aero space applications	Volume IX, Issue No:XII, ISSN NO: 2278-4632	Juni khyat Journal	Dec-2022
16	G.Aruna kumari	Design of area-efficient and highly reliable RHBD10T memory cell for aero space applications	Volume IX, Issue No:XII, ISSN NO: 2278-4632	Juni khyat Journal	Dec-2022
14.	N.Naveen kumar	efficient network on chip (noc) by using content-addressable memory (cam)	vol 12, issue 11,	Journal of Engineering Sciences	Nov-2021
15.	Dr. Y L Ajay kumar	efficient network on chip (noc) by using content-addressable memory (cam)	vol 12, issue 11,	Journal of Engineering Sciences	Nov-2021
16.	N.Naveen kumar	high-level optimization techniques for low-power high-performance booth multipliers	vol 12, issue 10	Journal of Engineering Sciences	Oct-2021

17.	Dr. Y L Ajay kumar	high-level optimization techniques for low- power high-performance booth multipliers	vol 12, issue 10	Journal of Engineering Sciences	Oct - 2021
-----	-----------------------	---	------------------	---------------------------------------	---------------

2. FDP/WORKSHOP/STTP/SEMINAR/WEBINAR

1. B Sai Mani, Participated in a three day Workshop on “Digital Design Of FPGA Through Verilog” during 19th Jun to 21st Jun 2021 organized by Department of ECE, Siddhartha Institute Of Engineering and Technology, Ibrahimpatnam.
2. Dr. E Lakshmi Prasad, Participated in a five days e-Workshop on “ Microwave Passive Circuits” during 17th Jan to 21th Jan 2022 organized by department of Electronics and Communication Engineering, Madanapalle institute of Technology & Science, Madhanapalle, Andhra Pradesh India.
3. G Mahesh, Participated in a five days offline FDP on “Introduction of Accreditation Mechanism & NBA Approach” during 19th Jun to 21st Jun 2021 organized by Department of ECE, Siddhartha Institute Of Engineering and Technology, Ibrahimpatnam.
4. V Aruna Kumari, Participated in a one week online Faculty Development Program on “Business intelligence & Big Data Analytic” during 9th Dec to 16th Dec 2021 organized by department of ECE, S.V Engineering (Autonomous), Tirupati, Andhra Pradesh.
5. K Adinarayana, Participated in an one week online FDP on "Artificial Intelligence" during 24th to 28th Aug 2021, organised by Dept of ECE, JNTU Ananthapuramu, Andhra Pradesh.

3. PATENTS

S. No.	Name of the Staff Member with Designation	Title of the Paper Page No.	Volume No, Issue No & ISSN/ISBN No	Name of the Journal	Year and Month of the Journal
1.	Dr. Y L Ajay kumar	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	July-2022
2	N.Naveen kumar	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	July-2022
3	Dr.M.Sreeraj	Implementation of video Compression Based on Spatial-Temporal Resolution Adaptation pp. 758-763	Volume 13, Issue No:07, :ISSN NO: 0377-9254	Journal of Engineering Sciences	July-2022

4	N.Naveen kumar	Design of novel adder for efficient image and video processing accelerators	Volume 13, Issue No:12, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
5	Dr. Y L Ajay kuma	Design of novel adder for efficient image and video processing accelerators	Volume 13, Issue No:12, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
6	N.Naveen kumar	Design of innovative 2-4 and 4-16 mixed-logic line decoders using cmos logic gates	Volume XIV, Issue XII, ISNN NO:0022-1945	Journal of inter disciplinary cycle research	Dec-2022
7	Dr. Y L Ajay kumar	Design of innovative 2-4 and 4-16 mixed-logic line decoders using cmos logic gates	Volume XIV, Issue XII, ISNN NO:0022-1945	Journal of inter disciplinary cycle research	Dec-2022
8	P.Chandana	Design of high performance VLSI multiplier with redundant binary compression	Volume 13, Issue No:12 :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
9	Dr. Y L Ajay kumar	Design of high performance VLSI multiplier with redundant binary compression	Volume 13, Issue No:12, :ISSN NO: 0377-9254	Journal of Engineering Sciences	Dec-2022
10	Y.Nirmala	Design of approximate multipliers by	Volume XIV, Issue No:XII, ISSN NO:	The international journal of	Dec-2022

		using hybrid technique	0866-936	analytical and experimental modal analysis	
11	Dr. Y L Ajay kumar	Design of approximate multipliers by using hybrid technique	Volume XIV, Issue No:XII, ISSN NO: 0866-936	The international journal of analytical and experimental modal analysis	Dec-2022
12	B.V.Aravind	Design of high performance prediction logic with dynamic timing error avoidance	Volume IX, Issue No:XII, ISSN NO: 1076-5131	Journal of applied science and computations	Dec-2022
13	Dr. Y L Ajay kumar	Design of high performance prediction logic with dynamic timing error avoidance	Volume IX, Issue No:XII, ISSN NO: 1076-5131	Journal of applied science and computations	Dec-2022

4. MEMBERSHIP

The following faculty are having Membership in Prestigious Organizations:

Sl.No	Name of the Faculty	Designation	Registered Professional Society	Professional Society Number/Year
1	Dr. Y. L. Ajay Kumar	Associate Professor	IAENG	187849
2	Dr. N. Chitra	Associate Professor	IAENG	2021
3	G. Aruna Kumari	Associate Professor	IAENG	2021
4	B. V. Aravind	Assistant Professor	IAENG	2021
5	S. Shabbir Ali	Assistant Professor	IAENG	2021
6	N Naveen Kumar	Assistant Professor	IAENG	2021

7	Y.B.Shabber Hussain	Assistant Professor	IAENG	2021
8	Y. Nirmala	Assistant Professor	IAENG	2021
9	P Chandana	Assistant Professor	IAENG	2021
10	C. Karishma	Assistant Professor	IAENG	2021
11	M.Mounika	Assistant Professor	IAENG	2021
12	P. Bhuvanewari	Assistant Professor	IAENG	2021
13	K Sajida	Assistant Professor	IAENG	2021
14	E Ganga Saraswathi	Associate Professor	IAENG	2021
15	N. Yerri Swamy	Associate Professor	IAENG	2021
19	S. Shameera Begam	Assistant Professor	IAENG	2021
17	M.Suguneswari	Assistant Professor	IAENG	2021
18	G.S .Baba Junaid	Assistant Professor	IAENG	2021
19	N.Sravani	Assistant Professor	IAENG	2021
20	Shaik Jubeda Begum	Assistant Professor	IAENG	2021
21	C.Obulesu	Assistant Professor	IAENG	2021
22	Dr.M.V Sreeraj	Associate Professor	IAENG	2021
23	Dr.M.Chandrasekhar Reddy	Associate Professor	IAENG	2021
24	M.Ramesh Naidu	Assistant Professor	IAENG	2021
25	R.K.Thanush	Assistant Professor	IAENG	2021
26	V.Hari Prasad	Assistant Professor	IAENG	2021
27	M.Rajesh	Assistant Professor	IAENG	2021
28	B.Saimani	Assistant Professor	IAENG	2021
29	G.Mahesh	Assistant Professor	IAENG	2021
30	T.Shashidhar	Assistant Professor	IAENG	2021
31	K.Adinarayana	Assistant Professor	IAENG	2021
32	J.Mohammad Ghazi	Assistant Professor	IAENG	2021

33	V.Damodar	Assistant Professor	IAENG	2021
34	T.swarnalatha	Assistant Professor	IAENG	2021
35	K.valibabu	Assistant Professor	IAENG	2021
36	P.Hassena Bhanu	Assistant Professor	IAENG	2021
37	C.Shankar	Assistant Professor	IAENG	2021
38	B.Bhagya	Assistant Professor	IAENG	2021
39	L.Prasad Naik	Assistant Professor	IAENG	2021
40	J.Phaneendra	Assistant Professor	IAENG	2021
41	J.Hema Madhuri	Assistant Professor	IAENG	2021

5. PLACEMENTS

The following is the data of IV ECE (2018-22 Batch) B.Tech student Placements.

S.No.	Organization	CTC	Count
1	CAPGEMINI	2.00	4
2	HCL	7.25	28
3	IBM	1.5	3
4	INFOSYS	1.6	3
5	MINDTREE	1.0	
6	TCS	2.16	30
7	TECHMAHINDRA	2.7	6
8	VECTONE INDIA MOBILE SERVICES PVT LTD	2.5	1
9	WIPRO	7	26

SL. No.	ENROLLMENT NO	NAME OF THE STUDENT PLACED	NAME OF THE EMPLOYER	APPOINTMENT LETTER REFERENCE NO. WITH DATE
1	182G1A0401	VINAY AKULETI	WIPRO	ALTS/2022/ECE/WIPRO/001
2	182G1A0403	BADIGINCHULA SAI SRI LATHA	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/001
3	182G1A0404	BANDARU CHAITANYA KUMAR	HCL	ALTS/2022/ECE/HCL/001

4	182G1A0405	BATHALA ROHINI	HCL	ALTS/2022/ECE/HCL/002
5	182G1A0406	BEEDUPALLI GNANENDRA REDDY	TCS	ALTS/2022/ECE/TCS/001
6	182G1A0410	CHAKALI DEEPTHI	TCS	ALTS/2022/ECE/TCS/002
7	182G1A0411	LIKHITHA CHALLA	IBM	ALTS/2022/ECE/IBM/001
8	182G1A0412	CHINREDDY BHARGAVI	TCS	ALTS/2022/ECE/TCS/003
9	182G1A0415	DABBARA INDU LATHA	WIPRO	ALTS/2022/ECE/WIPRO/002
10	182G1A0416	DASARI SAI PRANATHI	TCS	ALTS/2022/ECE/TCS/004
11	182G1A0419	BHANU CHANDRA JITTA	WIPRO	ALTS/2022/ECE/WIPRO/003
12	182G1A0420	ARCHANA KALIMISETTY	WIPRO	ALTS/2022/ECE/WIPRO/004
13	182G1A0423	KAVETI GEETHA RANI	TCS	ALTS/2022/ECE/TCS/005
14	182G1A0424	KODURU GULSHAN BANU	TCS	ALTS/2022/ECE/TCS/006
15	182G1A0425	KONDARAPALLI NARENDRA REDDY	WIPRO	ALTS/2022/ECE/WIPRO/005
16	182G1A0426	KONDETI JAYANANDITHA	HCL	ALTS/2022/ECE/HCL/003
17	182G1A0427	SAI KONETI	WIPRO	ALTS/2022/ECE/WIPRO/006
18	182G1A0429	LAKKISETTY PAVITRA	WIPRO	ALTS/2022/ECE/WIPRO/007
19	182G1A0430	MALAPATI SREEVANI	TCS	ALTS/2022/ECE/TCS/007
20	182G1A0433	REDDEMMA MANGALA	TCS	ALTS/2022/ECE/TCS/008

21	182G1A0434	MANNALA MEGHAMALA	WIPRO	ALTS/2022/ECE/WIPRO/008
22	182G1A0435	NAGARCHI MAHAMMAD	HCL	ALTS/2022/ECE/HCL/004
23	182G1A0437	NALLANI SWETHA	HCL	ALTS/2022/ECE/HCL/005
24	182G1A0438	NARAYANAREDDYGARI SAI PREETHI	HCL	ALTS/2022/ECE/HCL/006
25	182G1A0439	N. TEJASWANI	TCS	ALTS/2022/ECE/TCS/009
26	182G1A0440	PAMALA VIJAYA	WIPRO	ALTS/2022/ECE/WIPRO/009
27	182G1A0441	PATRA KALPANA	WIPRO	ALTS/2022/ECE/WIPRO/010
28	182G1A0442	PIDAPA SANKEERTHANA	TCS	ALTS/2022/ECE/TCS/010
29	182G1A0443	POLA JYOSTHNA PRIYA	CAPGEMINI	ALTS/2022/ECE/CAPGEMINI/001
30	182G1A0444	POLURU POOJITHA	TCS	ALTS/2022/ECE/TCS/011
31	182G1A0445	POTHULA INDRAJA	TCS	ALTS/2022/ECE/TCS/012
32	182G1A0447	RAVULA JAYASREE	IBM	ALTS/2022/ECE/IBM/002
33	182G1A0449	SHAIK AFRIN	TCS	ALTS/2022/ECE/TCS/013
34	182G1A0452	SHARANYA SIVAI AHGARI	WIPRO	ALTS/2022/ECE/WIPRO/011
35	182G1A0456	TUMMALA MEGHANA	HCL	ALTS/2022/ECE/HCL/007
36	182G1A0457	VARANASI MADHURI	INFOSYS	ALTS/2022/ECE/INFOSYS/001
37	182G1A0458	VEERAPU LALITHA	WIPRO	ALTS/2022/ECE/WIPRO/012
38	182G1A0459	YANNAPOOSALA CHANDANA	TCS	ALTS/2022/ECE/TCS/014

39	182G1A0460	YERRAGINNI ASHOK KUMAR GOWD	CAPGEMINI	ALTS/2022/ECE/CAPGEMINI/002
40	182G1A0461	RACHANA AKULA	TCS	ALTS/2022/ECE/TCS/015
41	182G1A0463	B. RAJESWARI	WIPRO	ALTS/2022/ECE/WIPRO/013
42	182G1A0465	MANJUNATH BANDI	WIPRO	ALTS/2022/ECE/WIPRO/014
43	182g1a0466	BANGI SAILAJA	TCS	ALTS/2022/ECE/TCS/016
44	182G1A0468	BIRAMULLAGARI ARIF KHAN	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/002
45	182G1A0469	BUDURU RAMYASREEE	HCL	ALTS/2022/ECE/HCL/008
46	182G1A0470	BYRAPURAM OBULANNAGARI VANITHA	HCL	ALTS/2022/ECE/HCL/009
47	182G1A0472	SIREESHA CHENNAPAREDDIGARI	TCS	ALTS/2022/ECE/TCS/017
48	182G1A0473	CHIDIGE BHUMIKA	MINDTREE	ALTS/2022/ECE/MINDTREE/001
49	182G1A0474	CHIRATALA YASHASWI	WIPRO	ALTS/2022/ECE/WIPRO/015
50	182G1A0476	DALAVAI SUPRIYA	HCL	ALTS/2022/ECE/HCL/010
51	182G1A0477	DASARI DEVIPRIYA	HCL	ALTS/2022/ECE/HCL/011
52	182G1A0479	DUDEKULA SHAHANAJ	WIPRO	ALTS/2022/ECE/WIPRO/016
53	182G1A0481	SAI VARSHA GANDLA	TCS	ALTS/2022/ECE/TCS/018
54	182G1A0483	GOLLA HEMA	HCL	ALTS/2022/ECE/HCL/012
55	182G1A0488	KAKARLA SAI PRIYA	TCS	ALTS/2022/ECE/TCS/019
56	182G1A0489	KAPPALA GANGA PRASAD	HCL	ALTS/2022/ECE/HCL/013

57	182G1A0491	KURUBA MANISHA	CAPGEMINI	ALTS/2022/ECE/CAPGEMINI/003
58	182G1A0496	SHIVA SHANKAR MUTHYALA	WIPRO	ALTS/2022/ECE/WIPRO/017
59	182G1A0497	NAPA NAVEEN	HCL	ALTS/2022/ECE/HCL/014
60	182G1A0498	NENAVATH MANIKANTA NAIK	WIPRO	ALTS/2022/ECE/WIPRO/018
61	182G1A04A0	PAMIDI MOHAMMED AKIEB	TCS	ALTS/2022/ECE/TCS/020
62	182G1A04A1	PASUPULA CHANDRAKALA	HCL	ALTS/2022/ECE/HCL/015
63	182G1A04A2	SRAVANI POTHU	TCS	ALTS/2022/ECE/TCS/021
64	182G1A04A3	PULLIPATI VENUGOPAL	HCL	ALTS/2022/ECE/HCL/016
65	182G1A04A4	PUVVADI NIKHILA	VECTONE INDIA MOBILE SERVICES PVT LTD	ALTS/2022/ECE/VECTONE/001
66	182G1A04A5	RAMAVATH SIRIPRASAD	CAPGEMINI	ALTS/2022/ECE/CAPGEMINI/004
67	182G1A04A6	RAVULACHERUVU AMEER SUHAIL	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/003
68	182G1A04A7	SAI RAHUL RAYADURGAM PALEGAR	TCS	ALTS/2022/ECE/TCS/022
69	182G1A04A8	RODDAM SHAIK ARSHAD	TCS	ALTS/2022/ECE/TCS/023
70	182G1A04A9	SARASWATHULA SAI HARSHITHA	HCL	ALTS/2022/ECE/HCL/017
71	182G1A04B0	SHAIK JAVID BASHA	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/004
72	182G1A04B1	SHAIK MOHAMMAD SADIQ	TCS	ALTS/2022/ECE/TCS/024
73	182G1A04B6	SURABOYANI KIRAN	TCS	ALTS/2022/ECE/TCS/025

74	182G1A04B7	THAMMINENI MEDARA SREEBHAVANI	WIPRO	ALTS/2022/ECE/WIPRO/019
75	182G1A04B8	THAYI SAI KAVYA	WIPRO	ALTS/2022/ECE/WIPRO/020
76	182G1A04B9	THIMMAREDDYGARI HEMANTH REDDY	WIPRO	ALTS/2022/ECE/WIPRO/021
77	182G1A04C2	BELLAM SATHEESH KUMAR	HCL	ALTS/2022/ECE/HCL/018
78	182G1A04C6	C. LAKSHMI KANTH NAIK	HCL	ALTS/2022/ECE/HCL/019
79	182G1A04C7	PAVAN KUMAR CHATTA	WIPRO	ALTS/2022/ECE/WIPRO/022
80	182G1A04C8	DOMMARA VENKATESH REVATHI	HCL	ALTS/2022/ECE/HCL/020
81	182G1A04c9	EDIGA SADHANA	HCL	<u>ALTS/2022/ECE/HCL/021</u>
82	182G1A04D0	SOUMYA KRUPA GAJJALA	TCS	ALTS/2022/ECE/TCS/026
83	182G1A04D1	GOLLA SAISWAPNA	HCL	ALTS/2022/ECE/HCL/022
84	182G1A04D4	GUDIPUTI SANGEETHA	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/005
85	182G1A04D5	SIVA SAI POOJITHA JALADANKI	WIPRO	ALTS/2022/ECE/WIPRO/023
86	182G1A04D7	KURUBA ARAVIND	TECHMAHINDRA	ALTS/2022/ECE/TECHMAHENDRA/006
87	182G1A04D8	MADINENI DIVYA SREE	TCS	ALTS/2022/ECE/TCS/027
88	182G1A04D9	MADINENI MANASA	HCL	ALTS/2022/ECE/HCL/023
89	182G1A04E0	MANAVULA PAVANI	WIPRO	ALTS/2022/ECE/WIPRO/024
90	182G1A04E3	MAYAKUNTLA SAI ESWAR	INFOSYS	ALTS/2022/ECE/INFOSYS/002
91	182G1A04E4	M.RUNISHA	HCL	ALTS/2022/ECE/HCL/024

92	182G1A04E6	NAGEELA HARCHITA	IBM	ALTS/2022/ECE/IBM/003
93	182G1A04E7	NARUGONDLA SYAM PRAKASH	INFOSYS	ALTS/2022/ECE/INFOSYS/003
94	182G1A04E8	T.POOJITHA CHOWDARY	TCS	ALTS/2022/ECE/TCS/028
95	182G1A04F0	JAYASIMHA REDDY PULA	TCS	ALTS/2022/ECE/TCS/029
96	182G1A04F1	T.LIKITHA	HCL	ALTS/2022/ECE/HCL/025
97	182G1A04F2	RODDARAPU NIKHITHA	HCL	ALTS/2022/ECE/HCL/026
98	182G1A04G1	BHAVANI SANKAR DEGALA	TCS	ALTS/2022/ECE/TCS/030
99	192G1A0405	BATHALA DIVYA	HCL	ALTS/2022/ECE/HCL/027
100	192G1A0406	G.PAVAN KUMAR	WIPRO	ALTS/2022/ECE/WIPRO/025
101	192G1A0413	VUGGAM YADAV	WIPRO	ALTS/2022/ECE/WIPRO/026
102	192G1A0422	TALLA VENKATESULU	HCL	ALTS/2022/ECE/HCL/028

6. INTERNSHIPS

IV B.Tech ECE student S Sai Kiran bearing Roll No. 182G1A04B6 has successfully completed the two weeks online internship on “Wireless Communication” From 1st to 14th November 2021 organized by dept., of Electronics and Communication Engineering, in association with Silicon Technology, Bangalore.

Certificate of Completion

This is to certify that Mr./Miss SURABOYANI SAI KIRAN (182G1A04B6) of B.Tech (ECE), students of ANANTHALAKSHMI INSTITUTE OF TECHNOLOGY AND SCIENCES, Itikalapalli(V), S.K. University, Ananthapuramu (Dist.) Andhra Pradesh has completed his/her Internship/Industrial training program on WIRELESS COMMUNICATIONS collaboration with SILICON TECHNOLOGY SOLUTIONS for a period of 01 NOV 2021 to 14 NOV 2021.

Their conduct during the training period was satisfactory and we wish them all success for their future endeavour.

Warm Regards


Course Coordinator

Address: 3053, 1st Main Rd, Gayatrinagar, Rajajinagar, Bengaluru, Karnataka 560055.

Web Site: <http://www.silicontechsolutions.com>